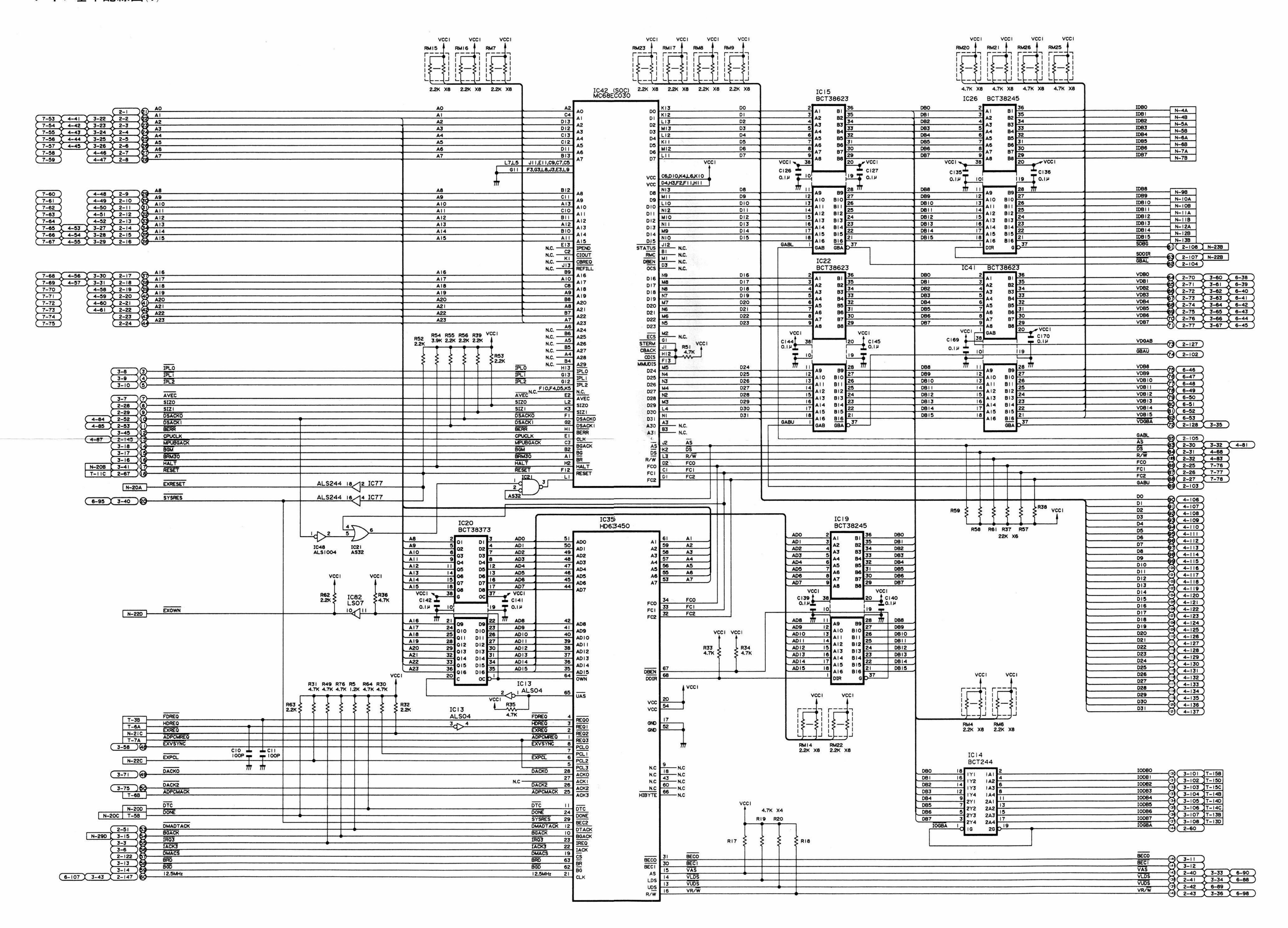
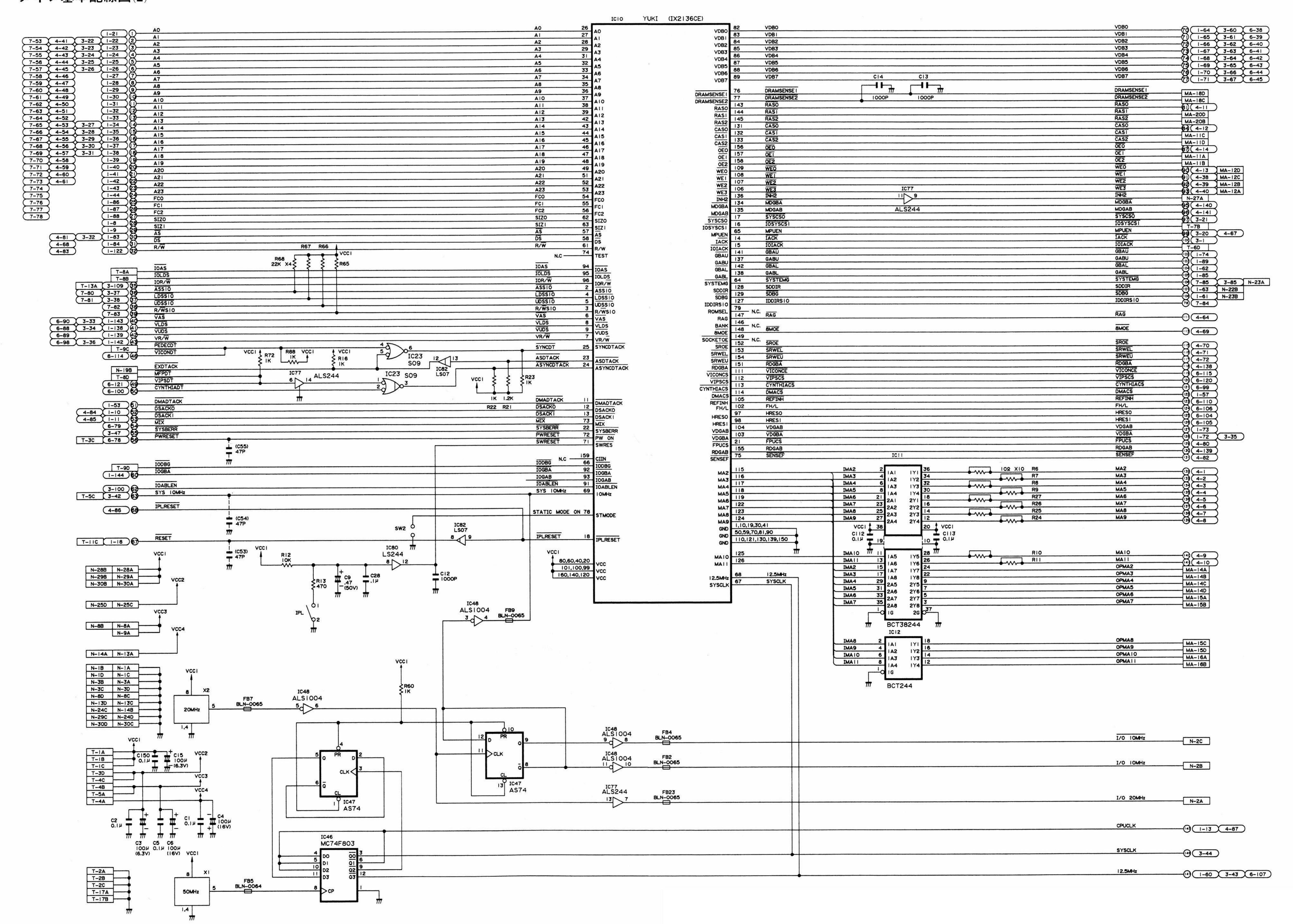
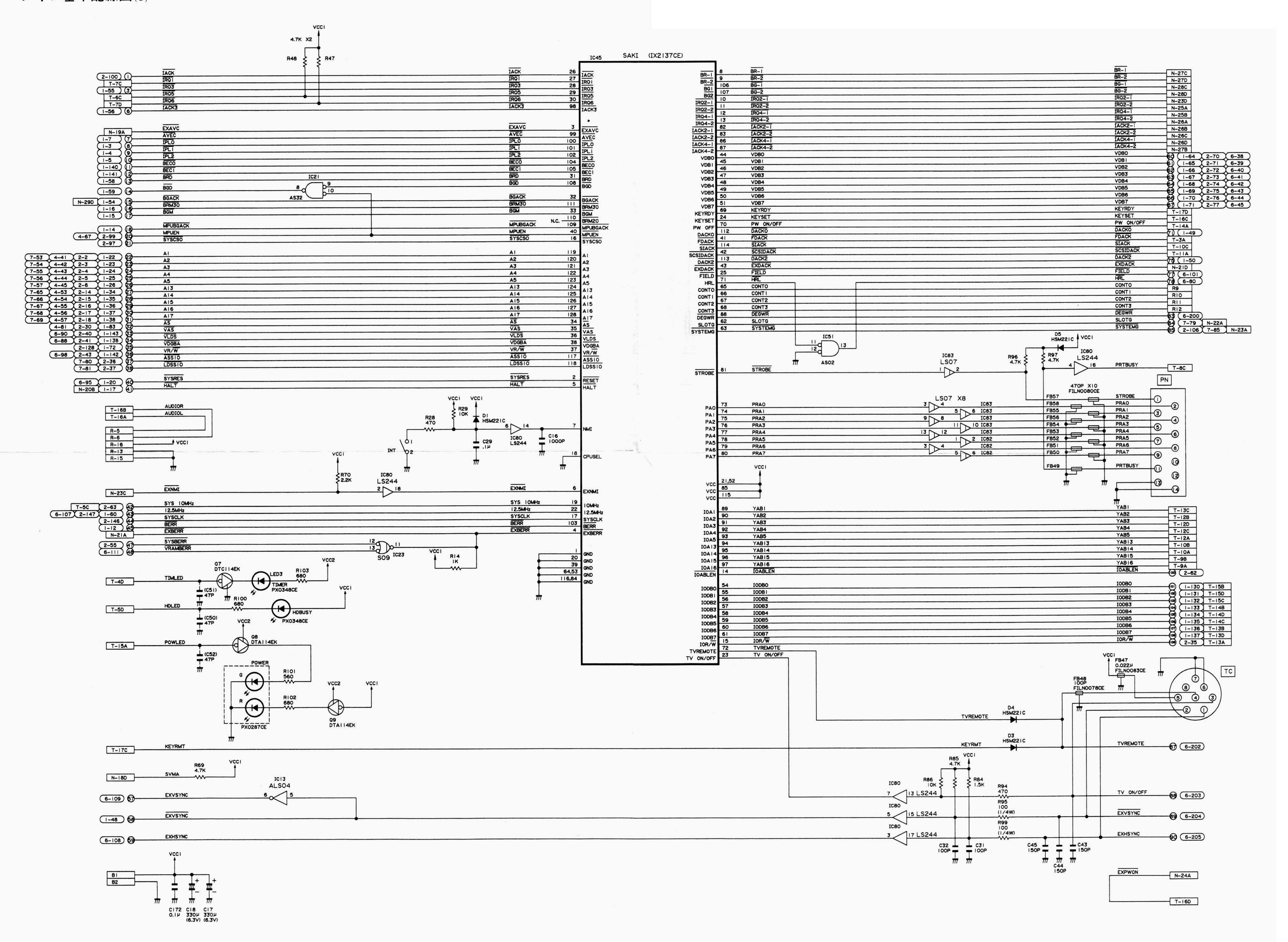
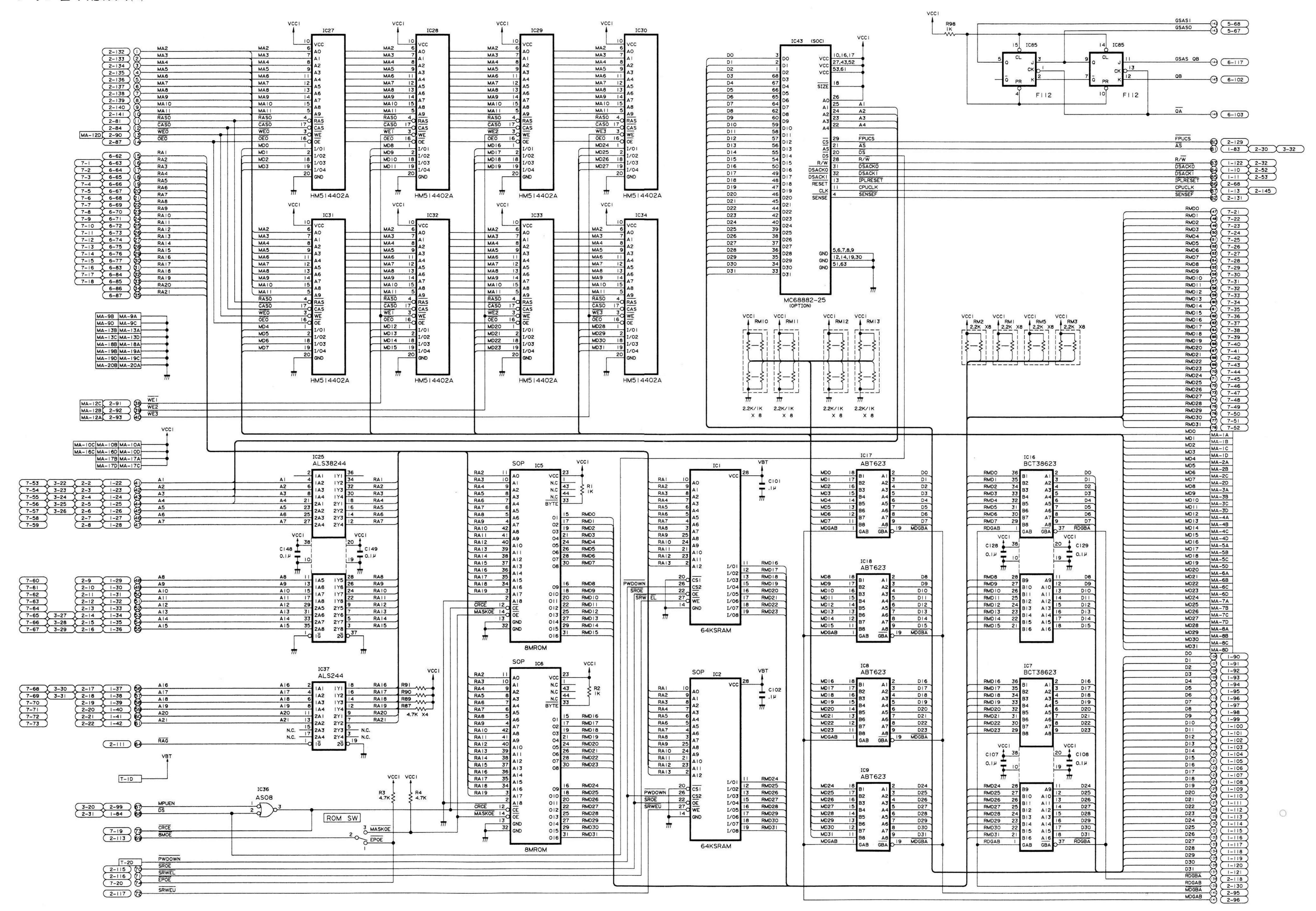
X68030 回路図

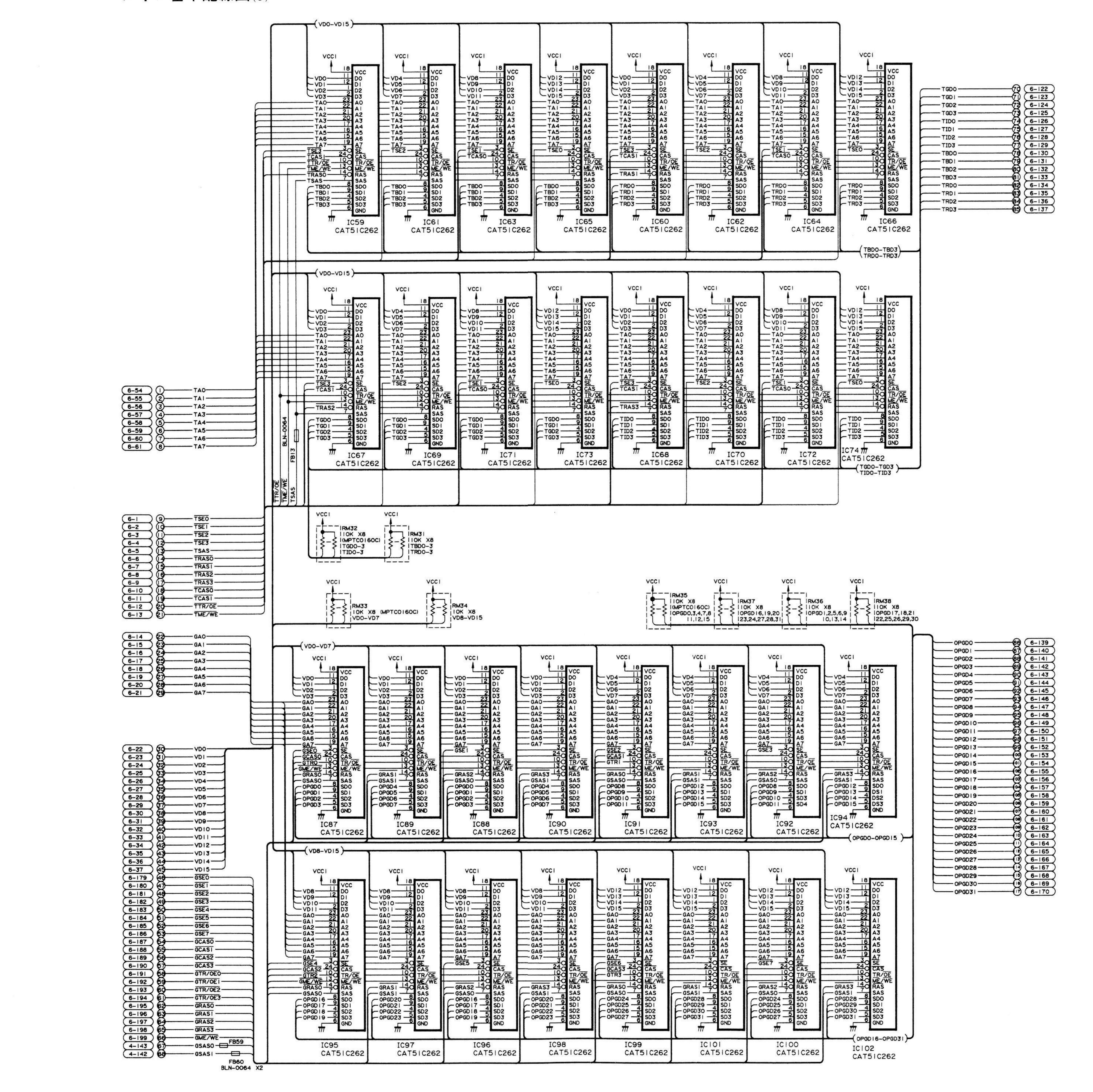


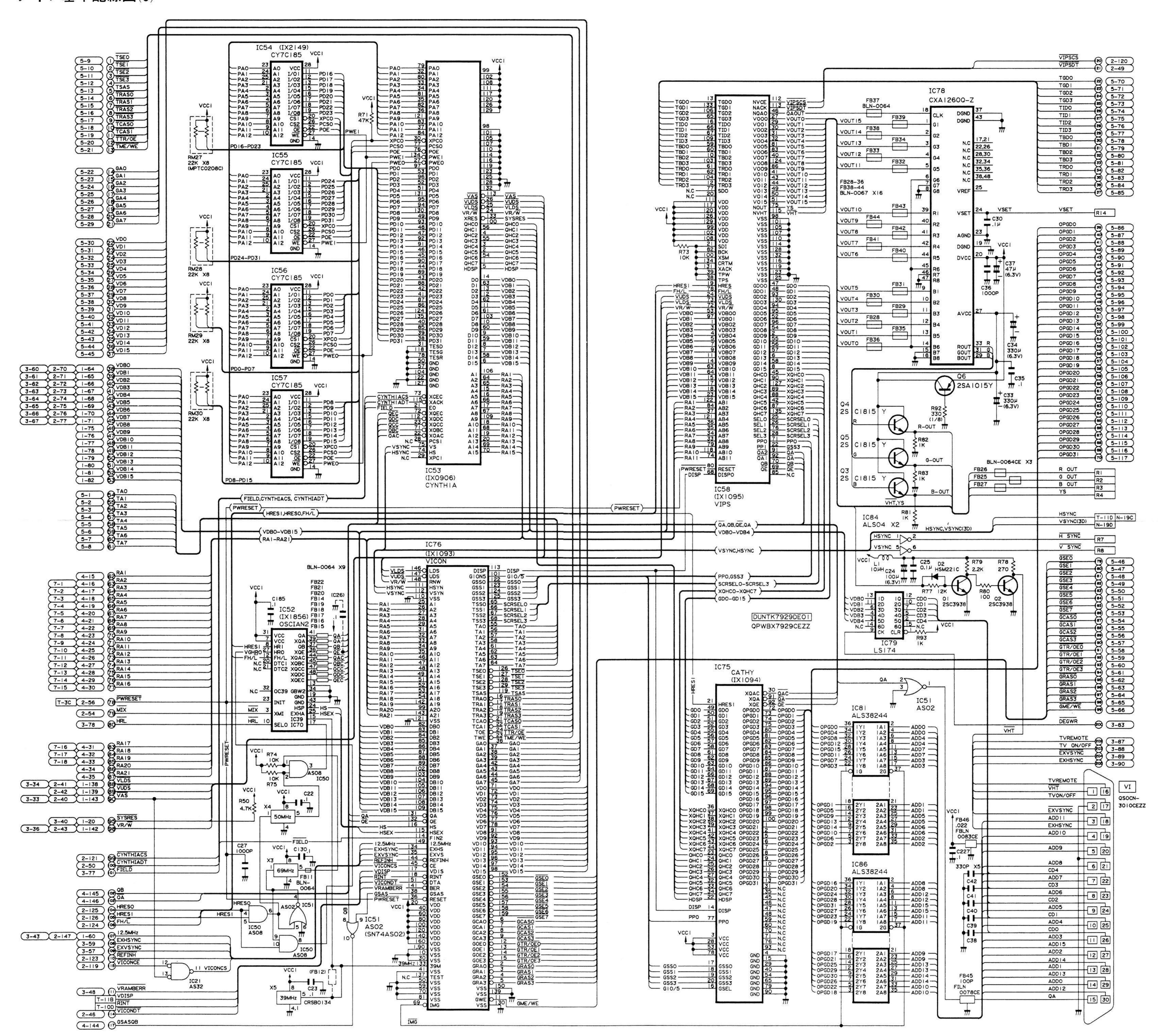
メイン基本配線図(2)

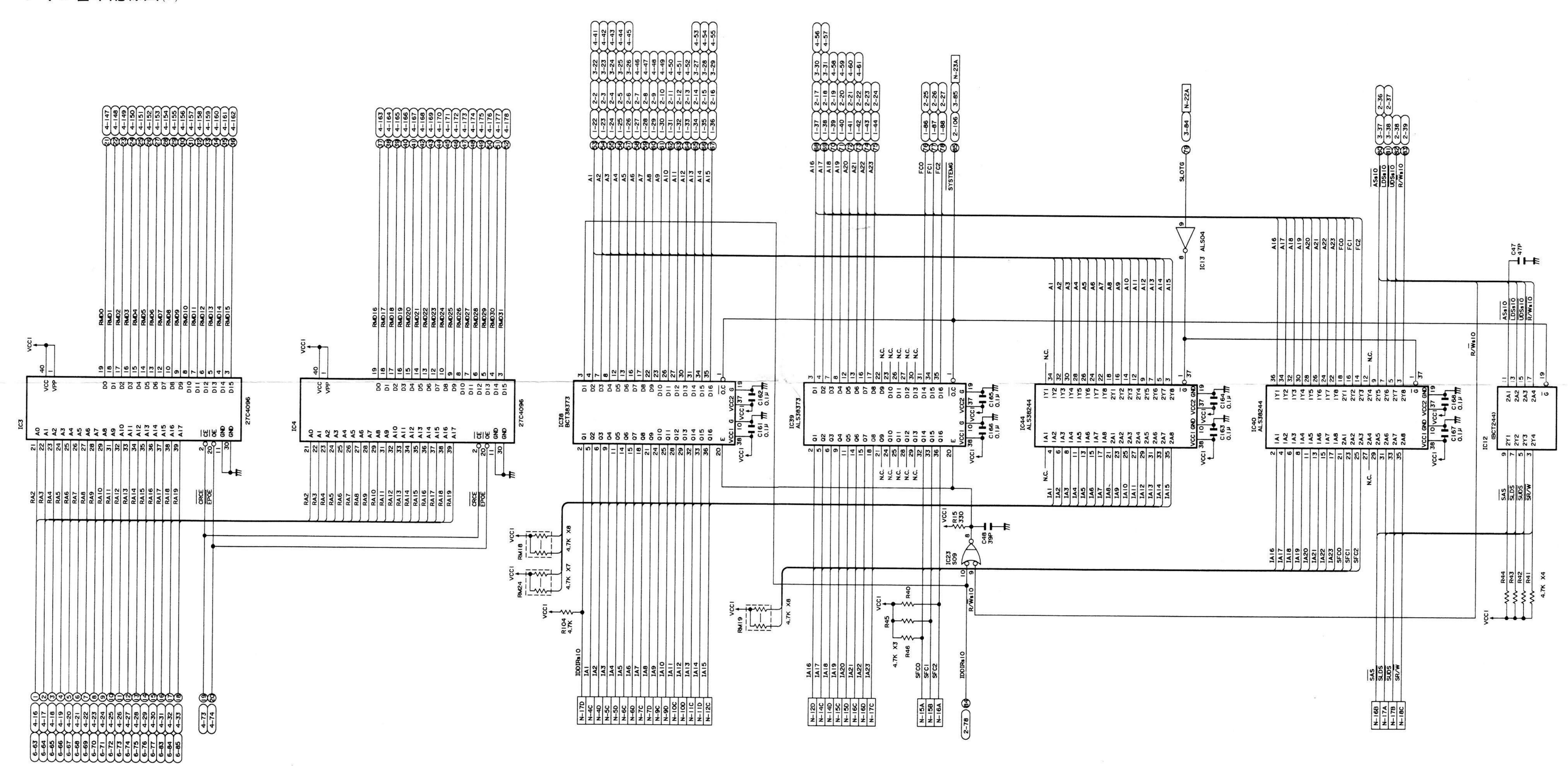


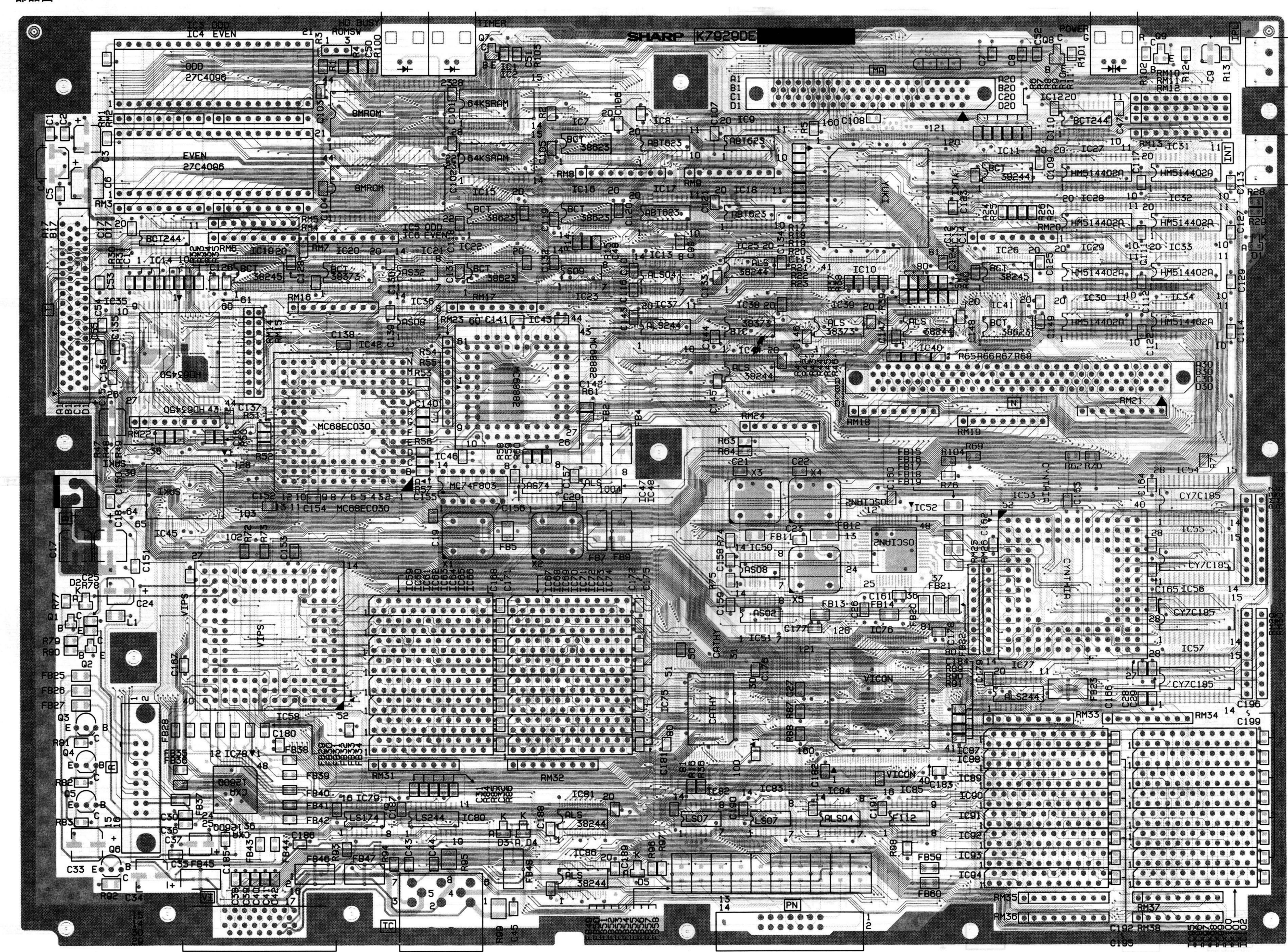


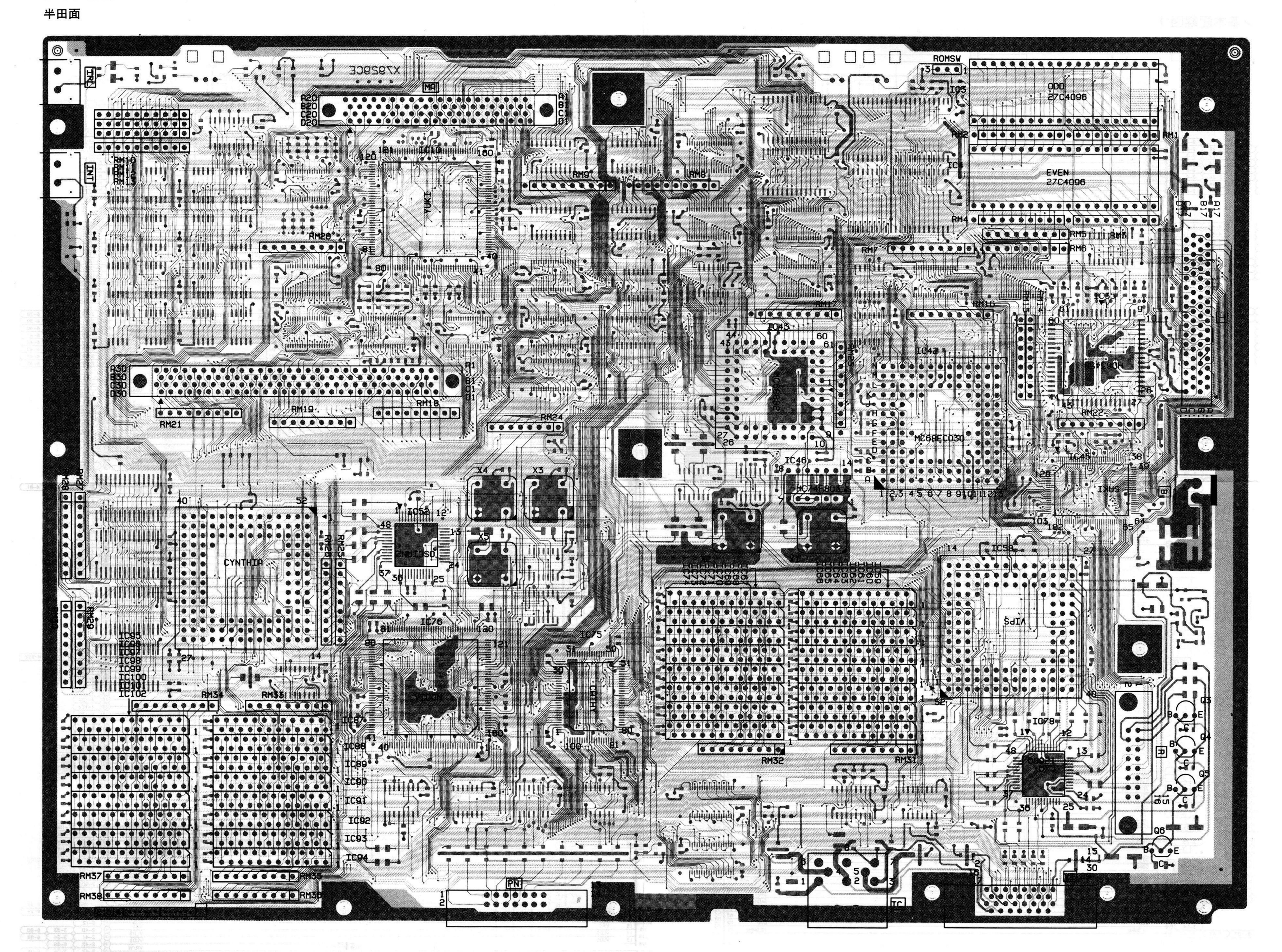


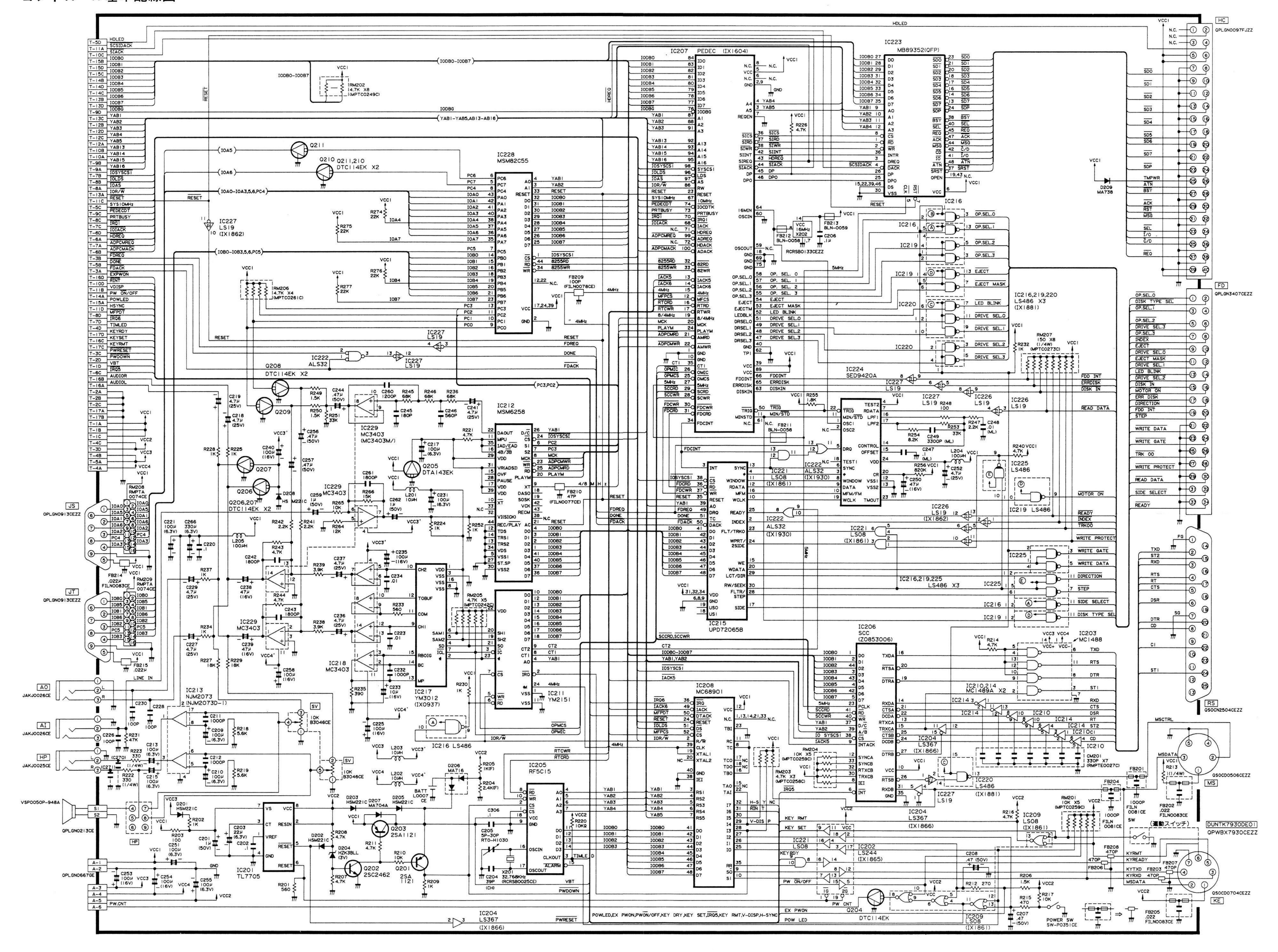


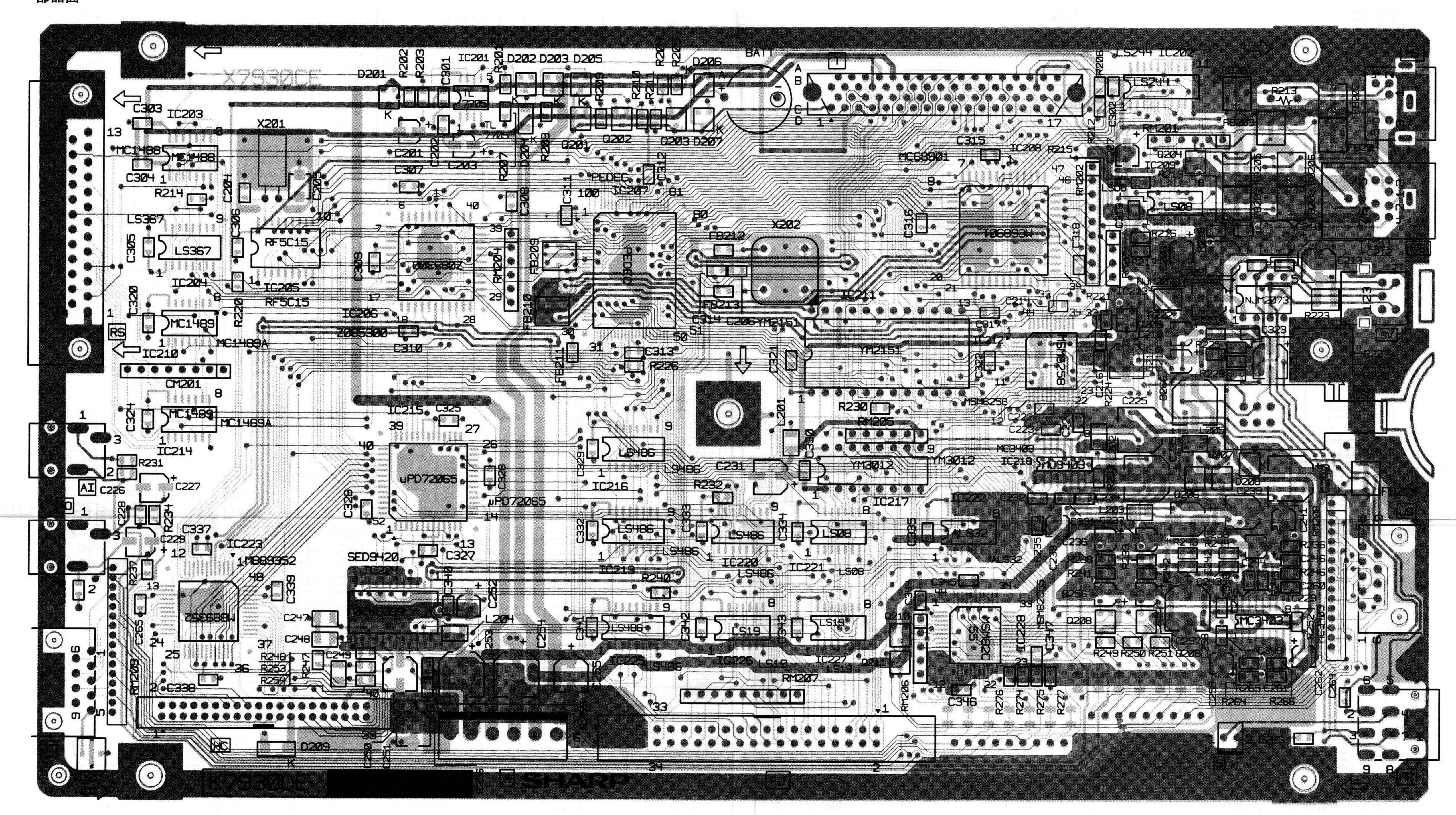


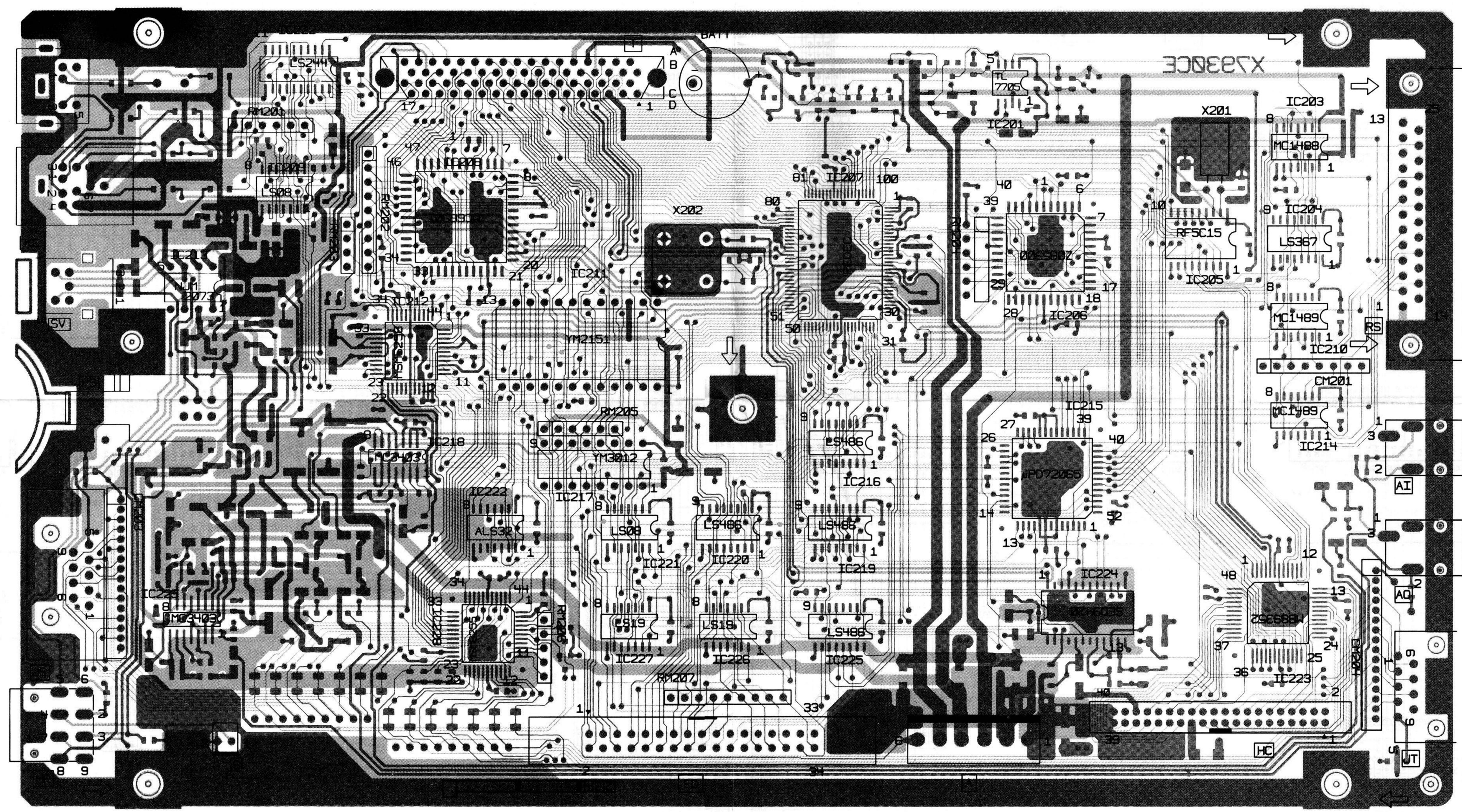


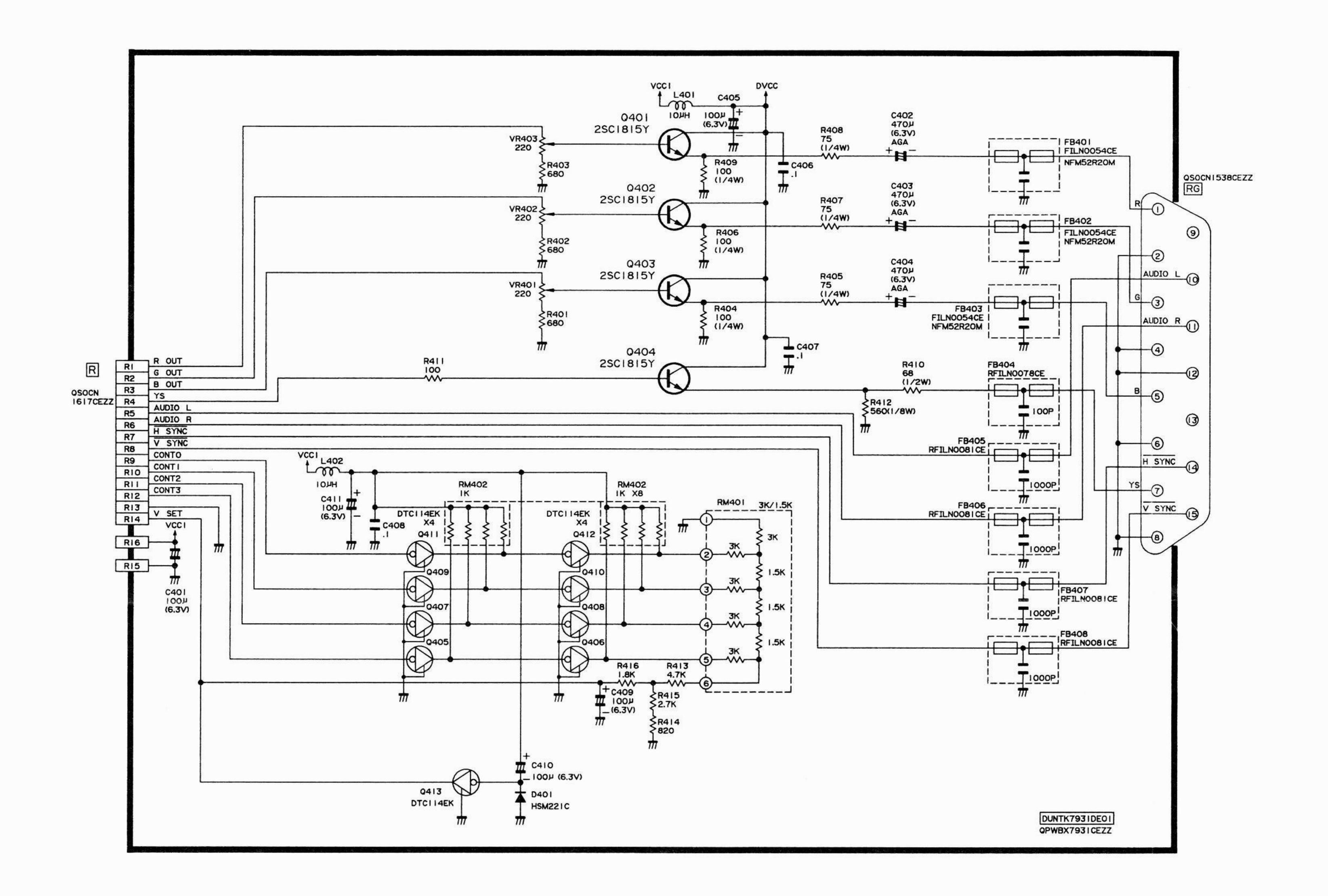






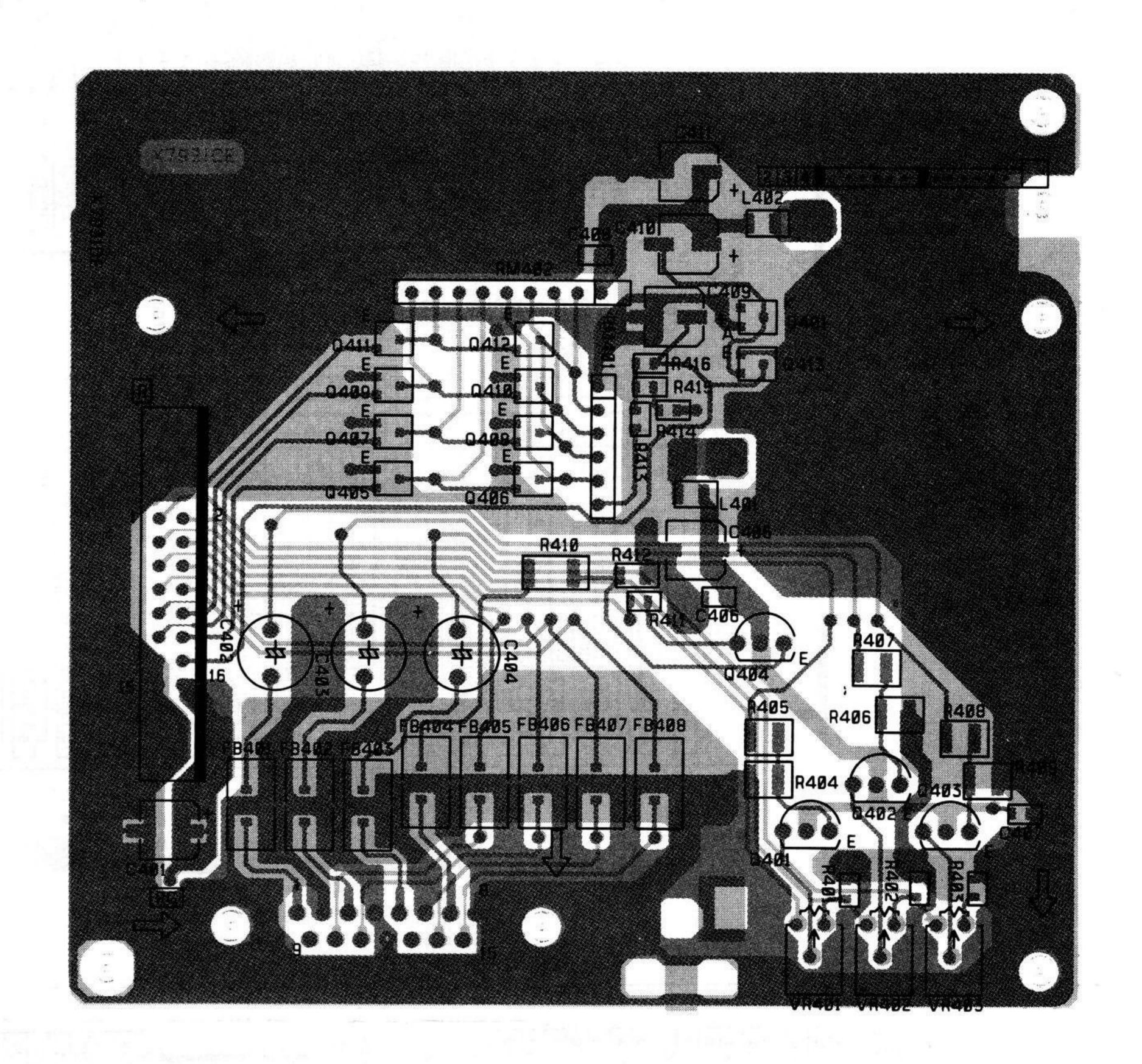




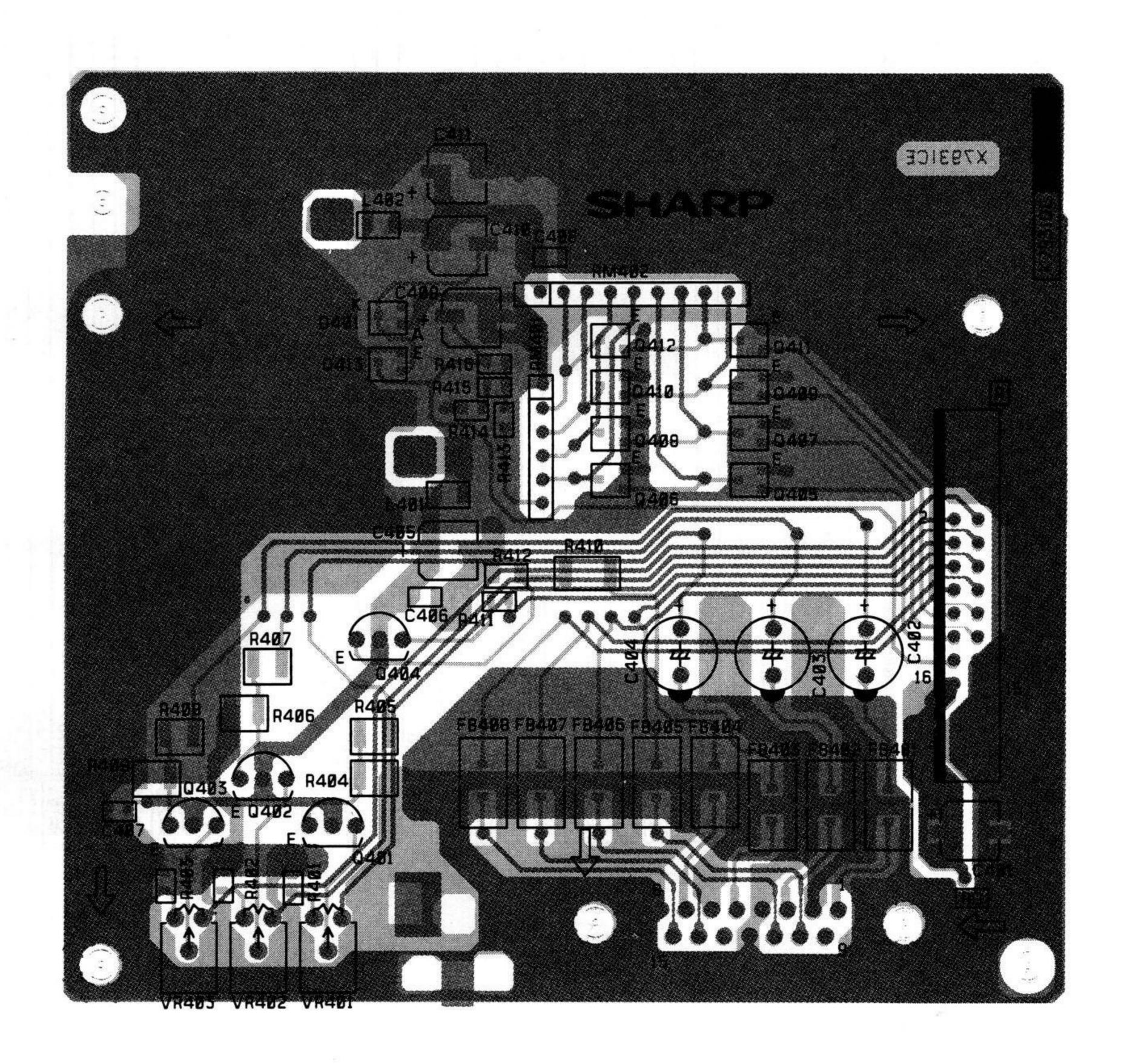


アナログ基板

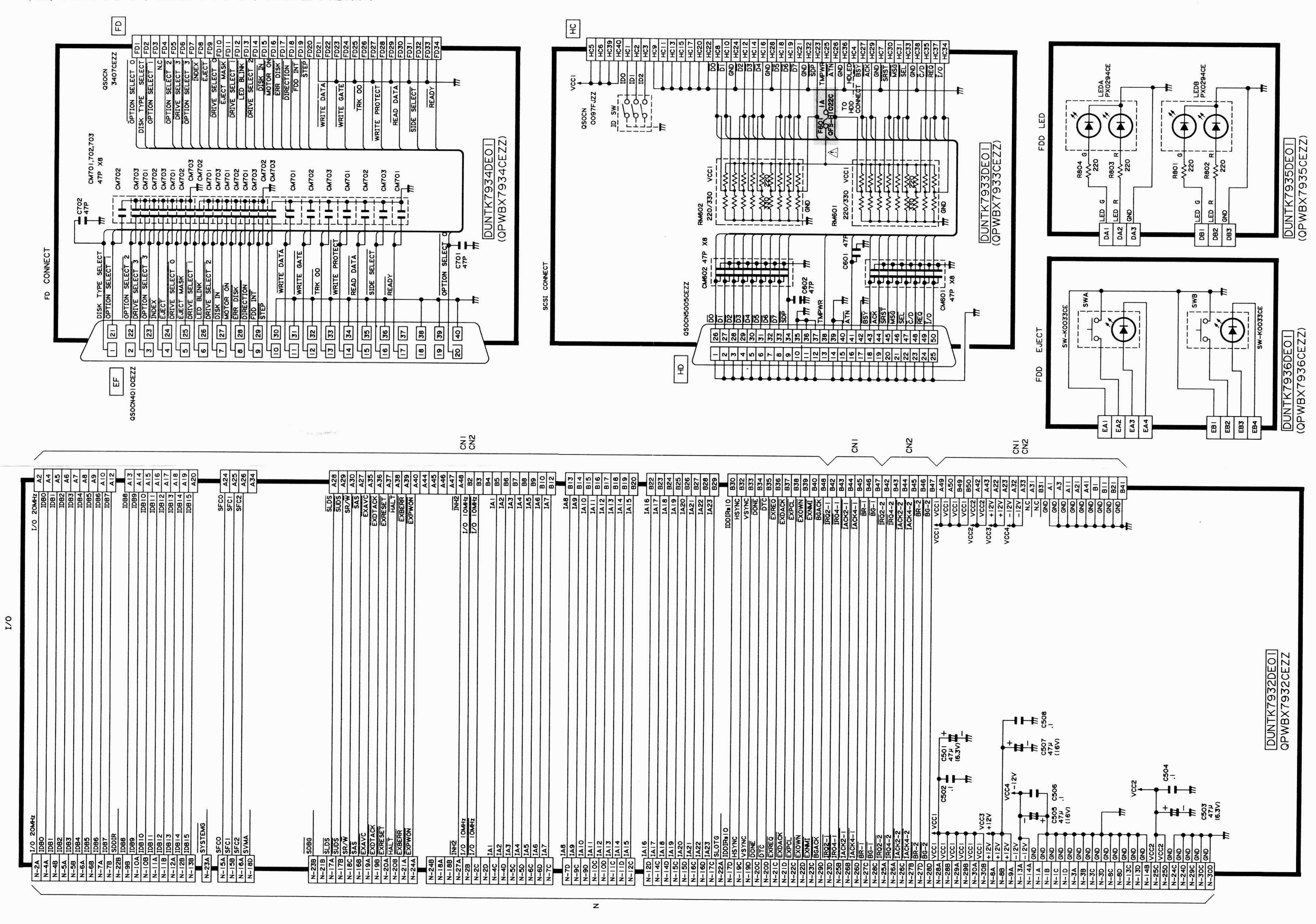
半田面



部品面

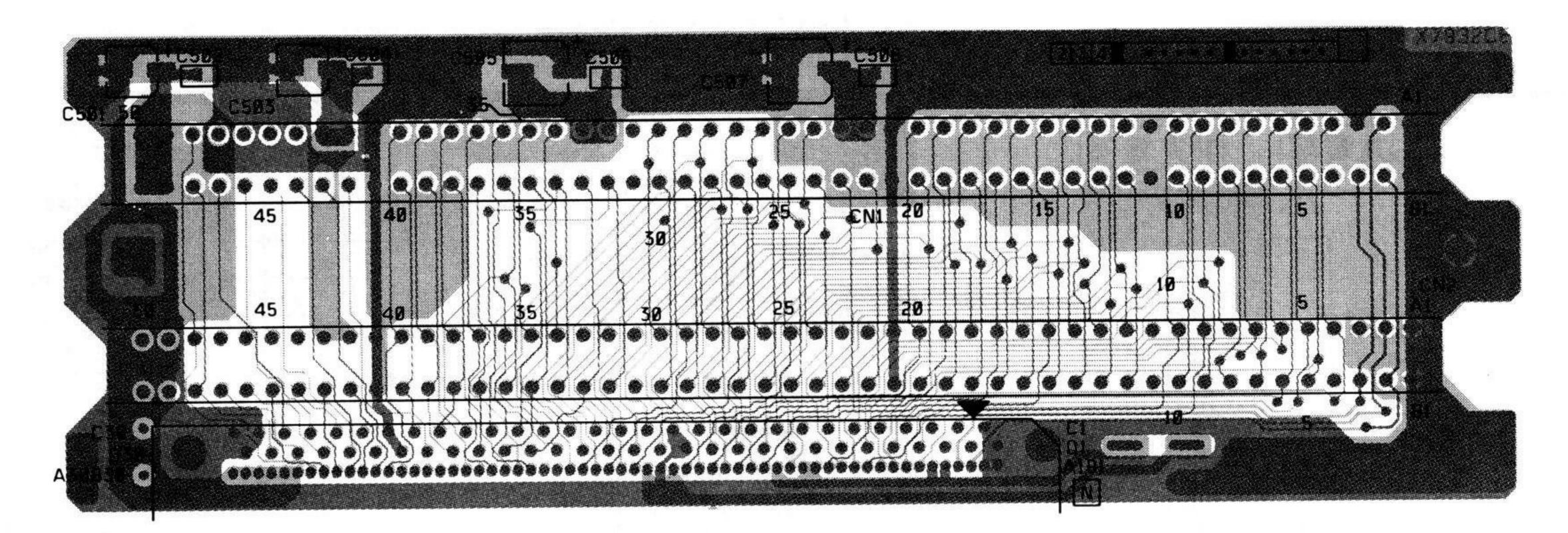


I/O、FDコネクタ、SCSIコネクタ、LED基本配線図

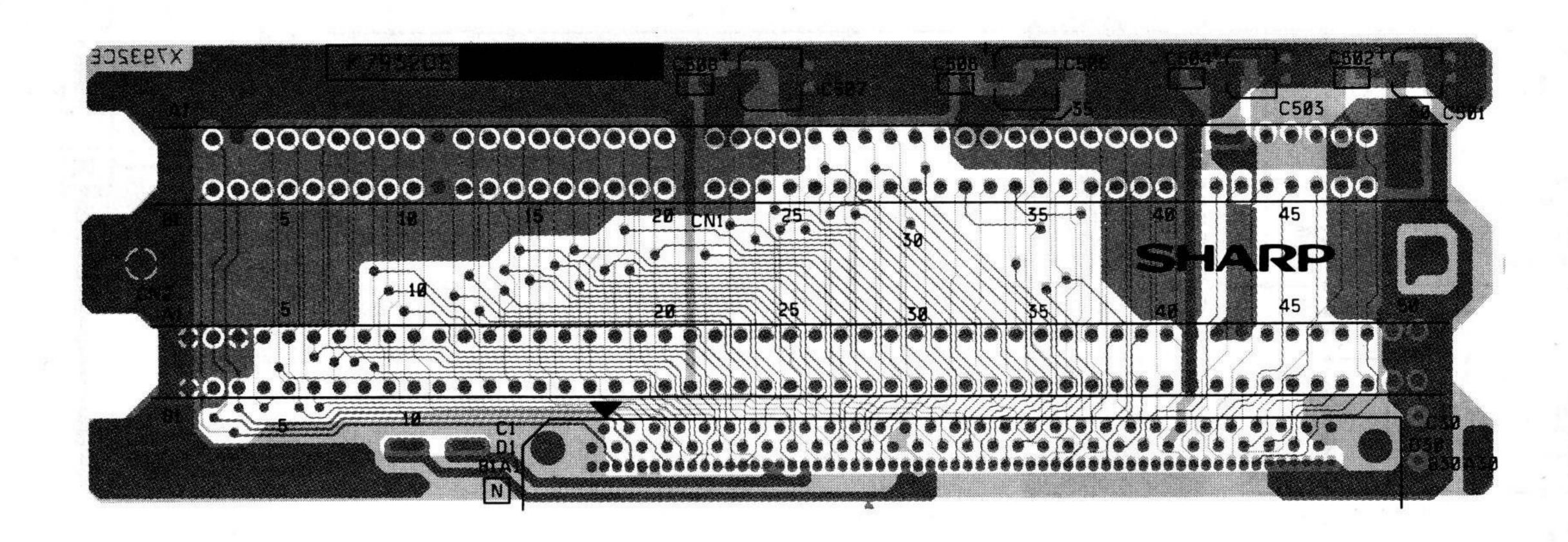


I/O基板

半田面

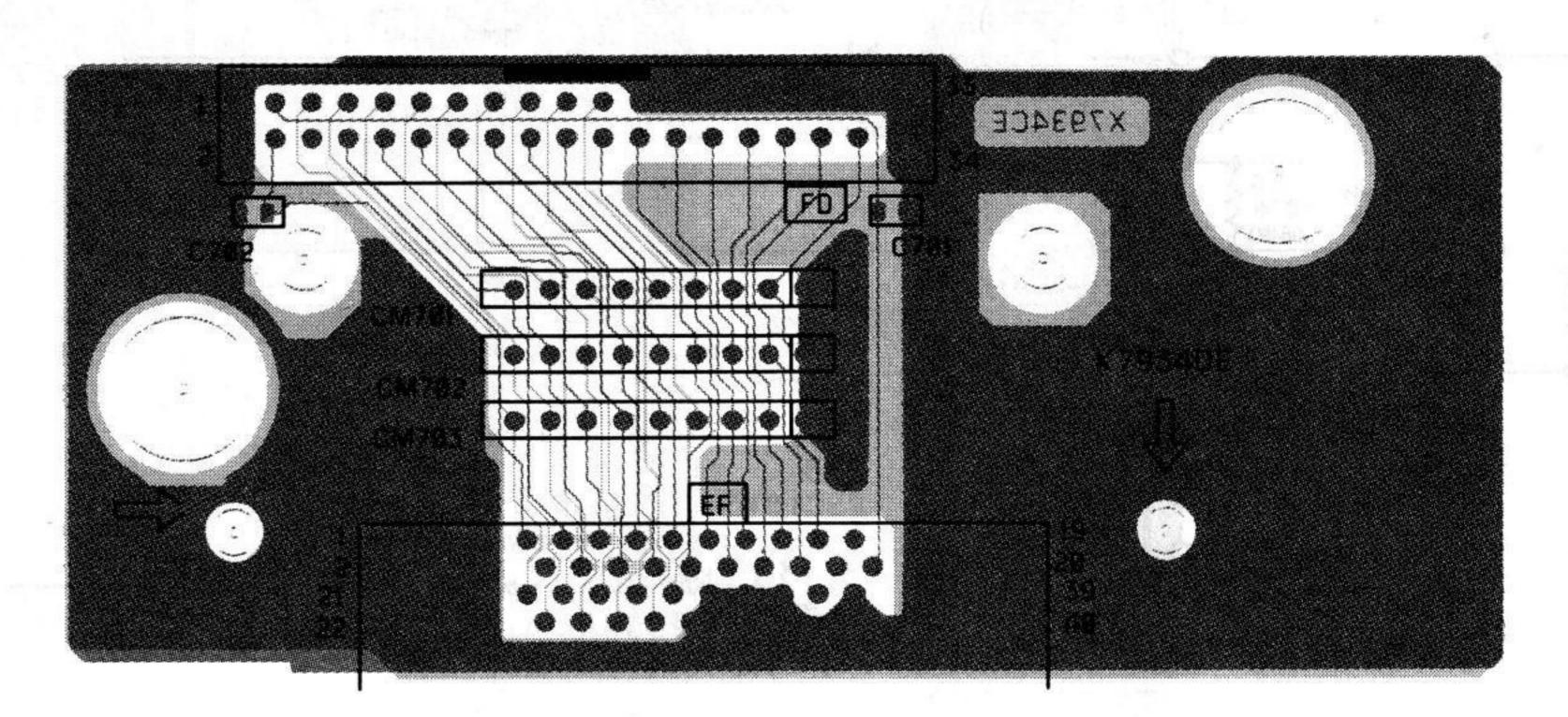


部品面

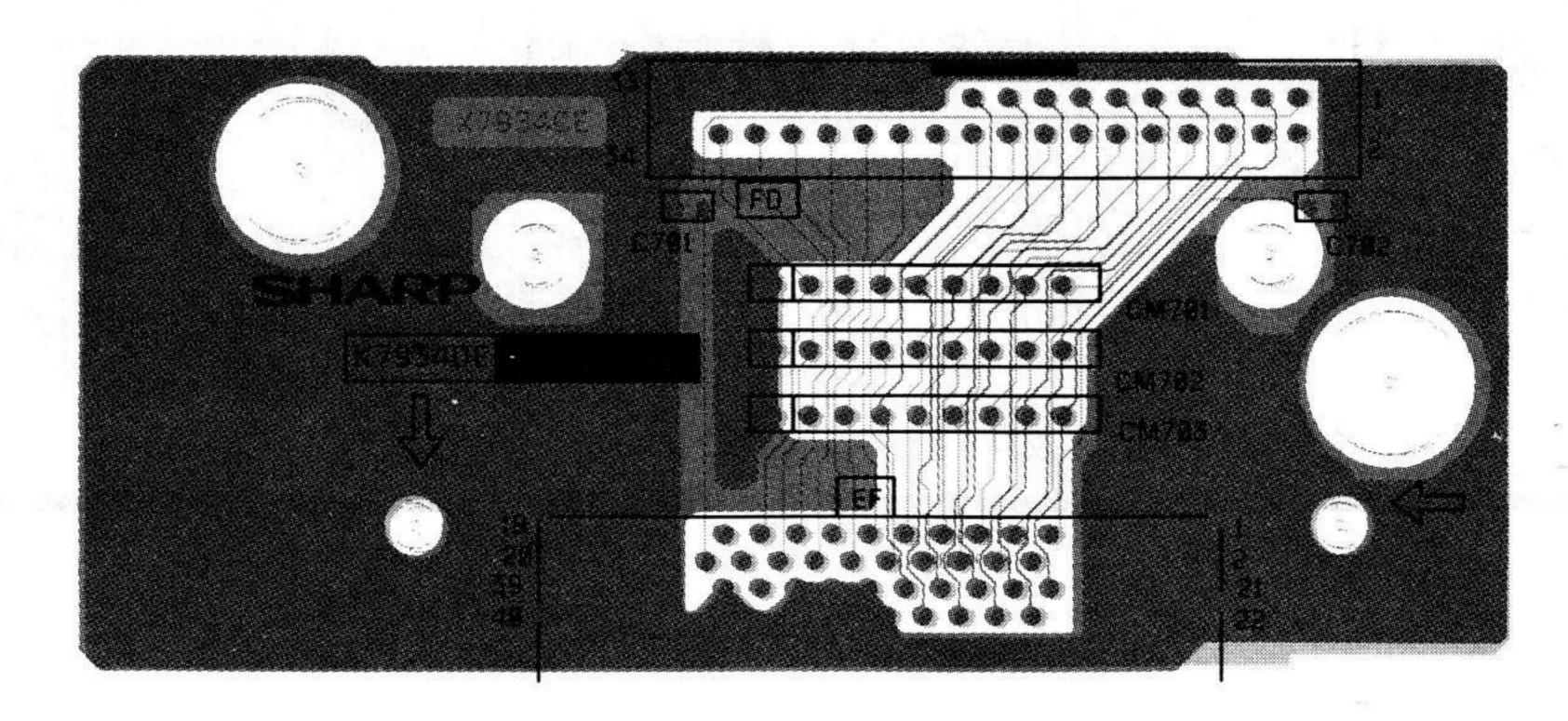


FDコネクタ基板

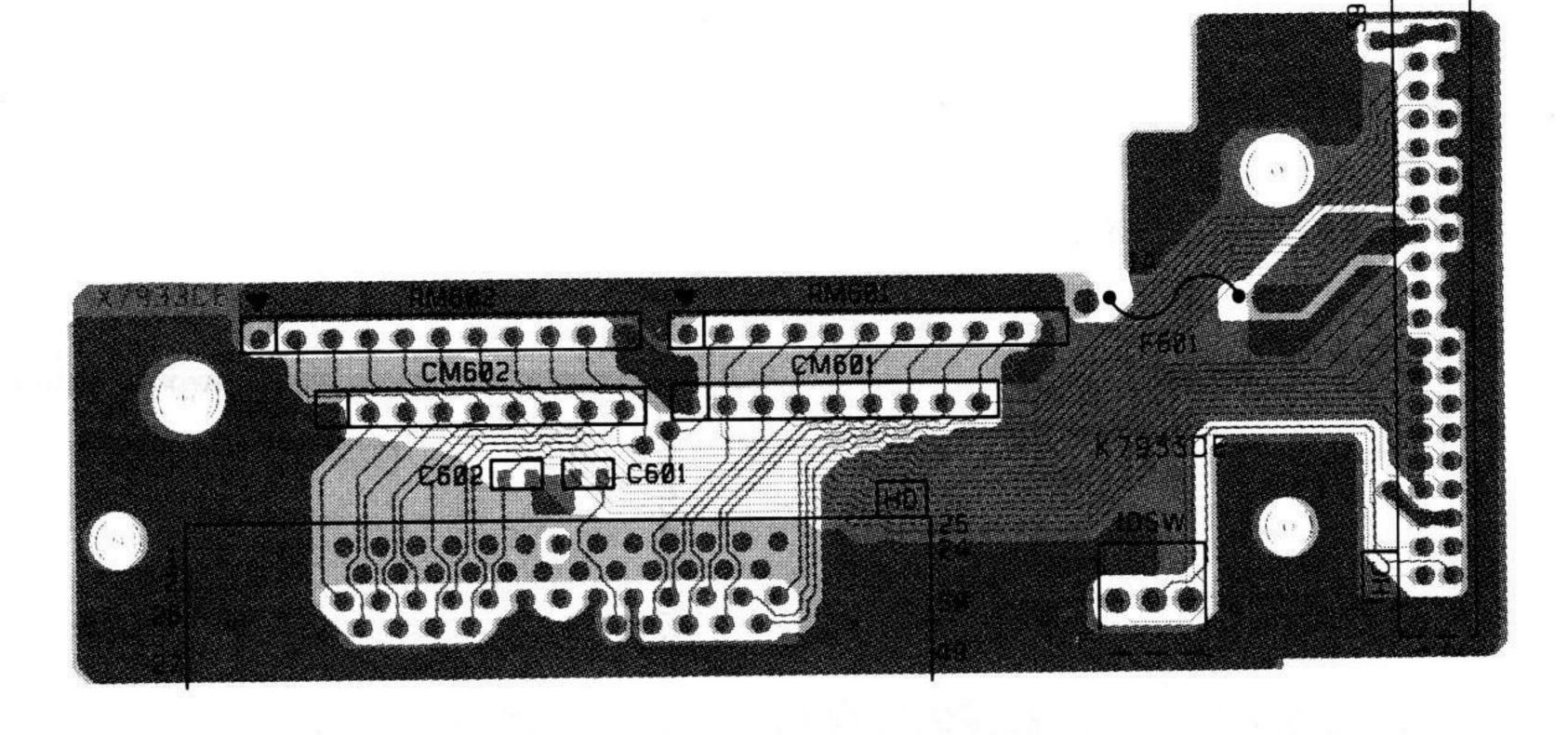
半田面



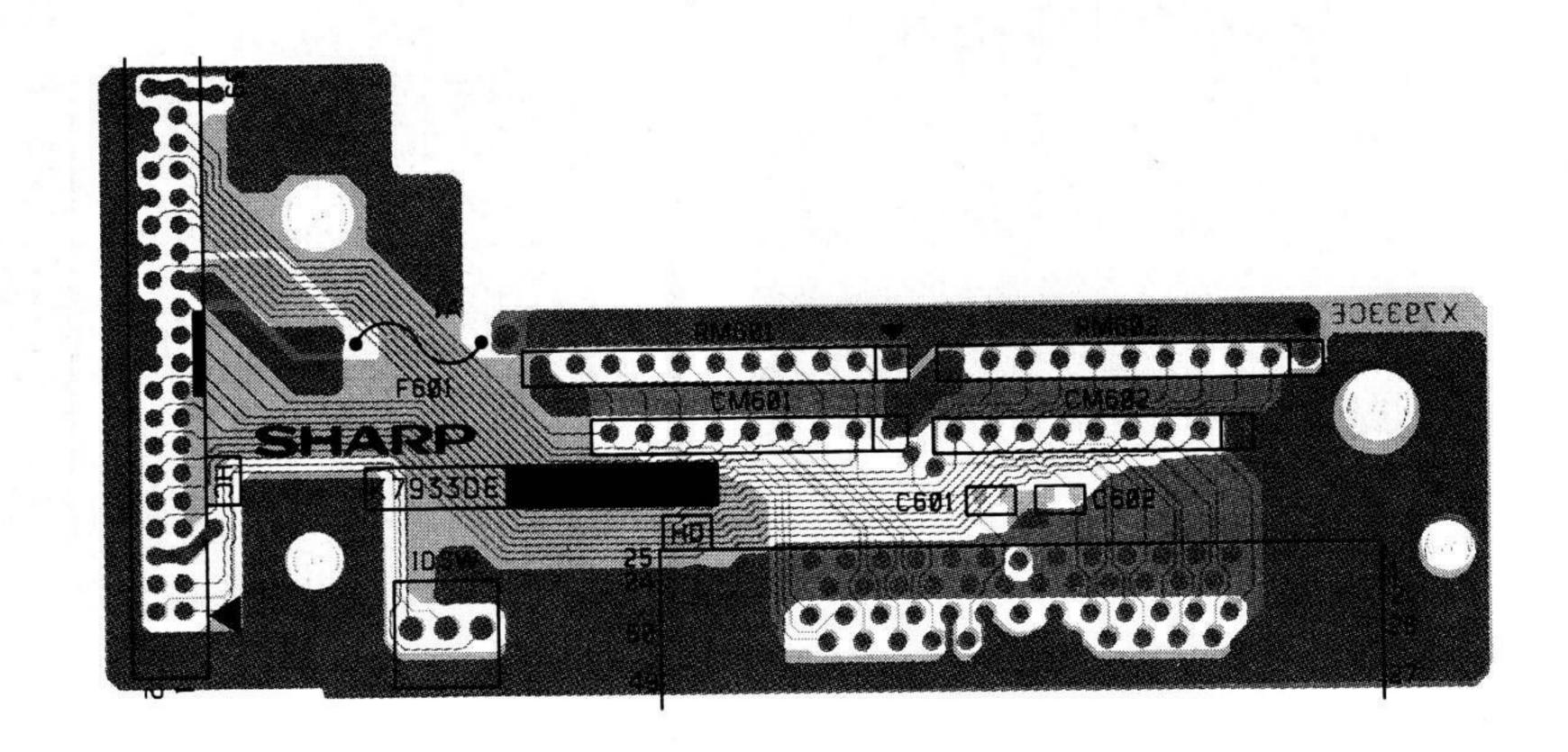
部品面



半田面

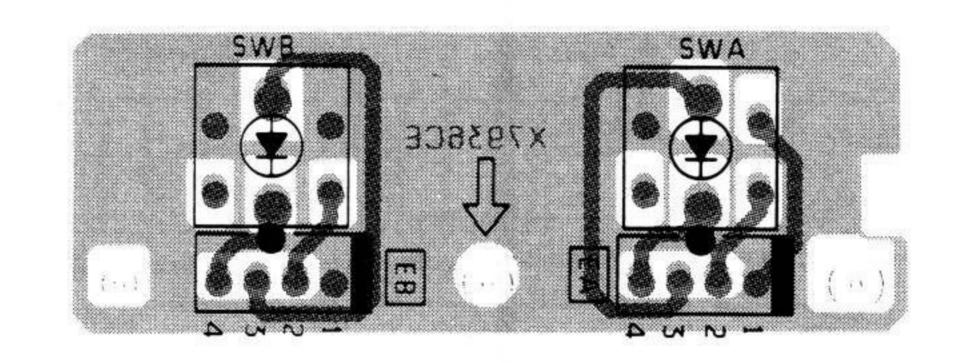


部品面

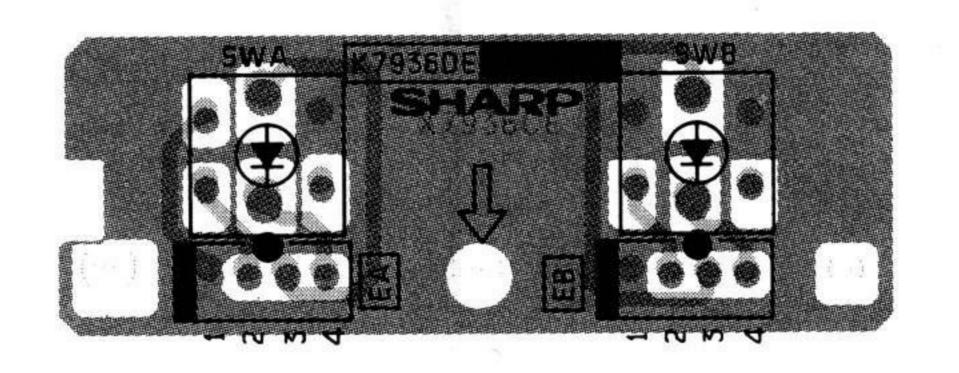


FDDイジェクト基板

半田面

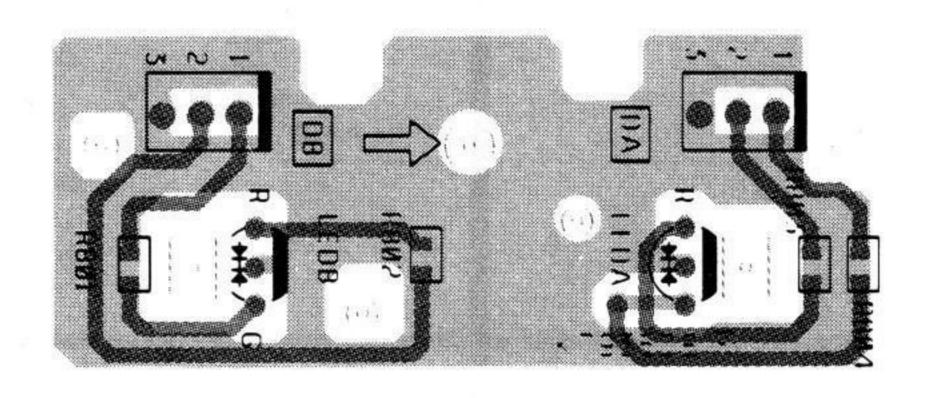


部品面



FDD基板

半田面



部品面

